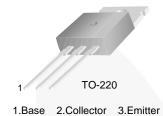


November 2014

# TIP41A / TIP41B / TIP41C NPN Epitaxial Silicon Transistor

#### **Features**

- · Medium Power Linear Switching Applications
- Complement to TIP42 Series



# **Ordering Information**

Part Number	Top Mark	Package	Packing Method
TIP41A	TIP41A	TO-220 3L (Single Gauge)	Bulk
TIP41B	TIP41B	TO-220 3L (Single Gauge)	Bulk
TIP41C	TIP41C	TO-220 3L (Single Gauge)	Bulk
TIP41CTU	TIP41C	TO-220 3L (Single Gauge)	Rail

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter		Value	Unit	
		TIP41A	60		
$V_{CBO}$	Collector-Base Voltage	TIP41B	80	V	
		TIP41C	100		
		TIP41A	60	$\Box$	
V <sub>CEO</sub>	Collector-Emitter Voltage	TIP41B	80	V	
		TIP41C	100		
V <sub>EBO</sub>	Emitter-Base Voltage		5	V	
I <sub>C</sub>	Collector Current (DC)		6	Α	
I <sub>CP</sub>	Collector Current (Pulse)		10	Α	
I <sub>B</sub>	Base Current		2	Α	
TJ	Junction Temperature		150	°C	
T <sub>STG</sub>	Storage Temperature Range	-65 to 150	°C		

# **Thermal Characteristics**

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit
В	Collector Dissipation (T <sub>C</sub> = 25°C)	65 W	
P <sub>C</sub>	Collector Dissipation (T <sub>A</sub> = 25°C)	2	VV

# **Electrical Characteristics**

Values are at  $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Max.	Unit
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage <sup>(1)</sup>	TIP41A		60		
		TIP41B	$I_C = 30 \text{ mA}, I_B = 0$	80		V
		TIP41C		100		
I <sub>CEO</sub> Co	Collector Cut-Off Current	TIP41A	$V_{CE} = 30 \text{ V}, I_{B} = 0$		0.7	
		TIP41B / TIP41C	V <sub>CE</sub> = 60 V, I <sub>B</sub> = 0		0.7	mA
I <sub>CES</sub>	Collector Cut-Off Current	TIP41A	$V_{CE} = 60 \text{ V}, V_{EB} = 0$		400	μΑ
		TIP41B	$V_{CE} = 80 \text{ V}, V_{EB} = 0$		400	
		TIP41C	V <sub>CE</sub> = 100 V, V <sub>EB</sub> = 0		400	
I <sub>EBO</sub>	Emitter Cut-Off Current		$V_{EB} = 5 \text{ V}, I_{C} = 0$		1	mA
h <sub>FE</sub>	DC Current Gain <sup>(1)</sup>		$V_{CE} = 4 \text{ V}, I_{C} = 0.3 \text{ A}$	30		
			$V_{CE} = 4 \text{ V}, I_{C} = 3 \text{ A}$	15	75	
V <sub>CE</sub> (sat)	at) Collector-Emitter Saturation Voltage <sup>(1)</sup>		$I_C = 6 \text{ A}, I_B = 600 \text{ mA}$		1.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(1)</sup>		$V_{CE} = 4 \text{ V}, I_{C} = 6 \text{ A}$		2.0	V
f <sub>T</sub>	Current Gain Bandwidth Product		$V_{CE} = 10 \text{ V}, I_{C} = 500 \text{ mA},$ f = 1 MHz	3.0		MHz

### Note:

1. Pulse test: pw  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2%.

# **Typical Performance Characteristics**

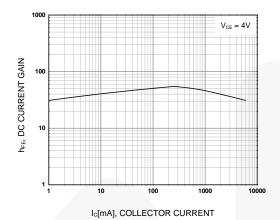


Figure 1. DC Current Gain

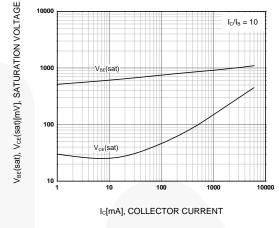


Figure 2. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

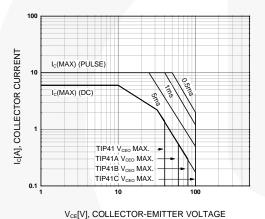


Figure 3. Safe Operating Area

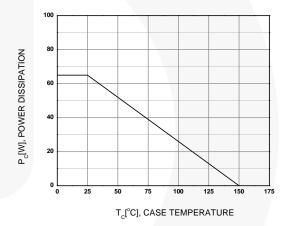
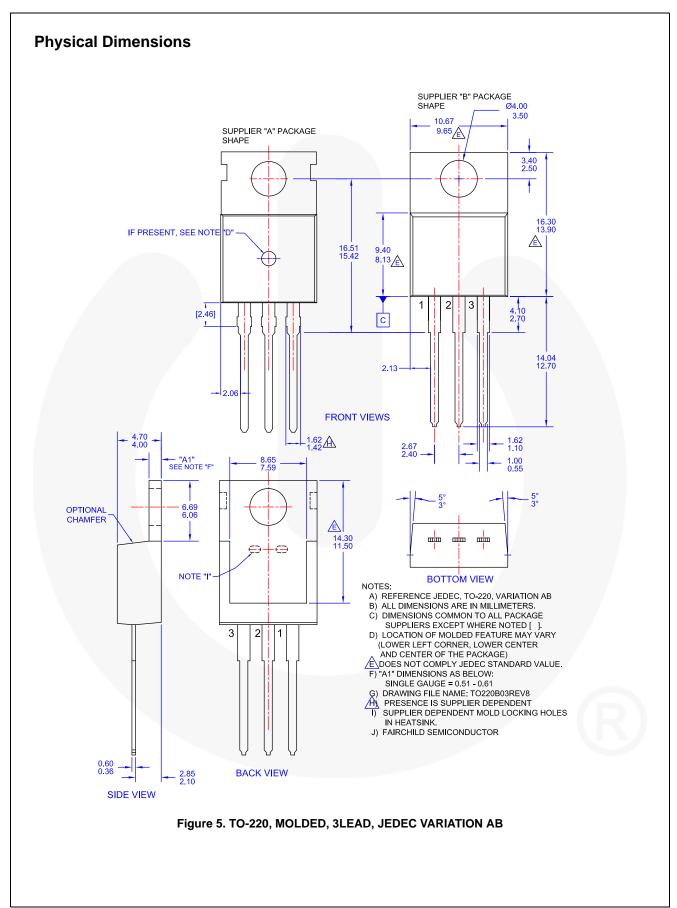


Figure 4. Power Derating







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No Identification Needed Full Production		Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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