HT-ICE Interface Card
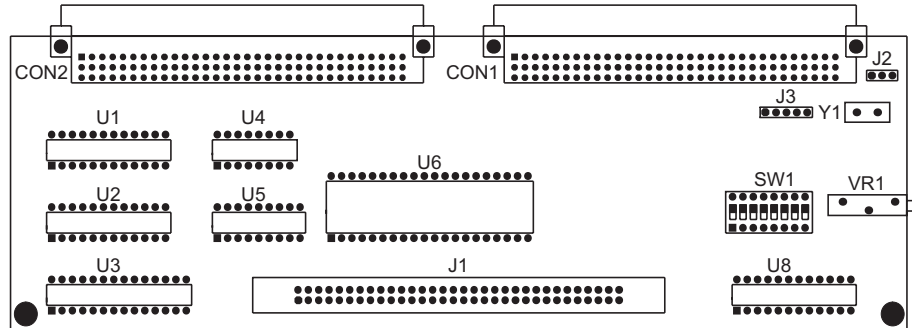# Reference Manual

November 2006

Contents

Introduction

The HT-ICE hardware emulator is the main hardware tool to provide designers with a method of fully emulating the functions of the MCU. Although collectively known as the HT-ICE, the emulator is supplied in various types, each with its own part number, the required one depends upon which MCU device is being used. To ensure that designers have a convenient and easy way of connecting the HT-ICE to their application hardware or target board, various interface cards, each with different part numbers, are also provided. These interface cards, which directly plug into the front socket of the HT-ICE, provides connectors for easy connection to the user's application hardware as well as any necessary additional switches and indicators necessary for full emulation.

Note that as new devices are continually being released by Holtek, which may require new interface cards, for the latest information it is suggested that the Holtek website is consulted.

CPCB48E000004A Interface Card

This interface card must be used in conjunction with the correct ICE Hardware Emulator. If the UART function is to be emulated then only later ICE versions should be used.

The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on J2 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR1. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection.

The J1 connector provides the I/O port connections as well as other pins. The DIP switch, SW1, should be set according to which device is selected and in accordance with the following table:

| SW1	1	2	3	4	5	6	7
HT48X05A-16 HT48X06A-1-16 HT48X08A-1-16	OFF	OFF	ON	OFF	ON	OFF	OFF
HT48X05A-18 HT48X06A-1-18 HT48X08A-1-18 HT48E06-18/20	OFF	OFF	ON	OFF	ON	OFF	OFF
HT48X07A-1-24 HT48X09A-1-24 HT48X10A-1-24 HT48E10-24	OFF	OFF	ON	OFF	ON	OFF	OFF
HT48X30A-24/28 HT48E30-24/28	ON	OFF	OFF	ON	OFF	OFF	OFF
HT48X50A-1-28 HT48E50-28	ON	OFF	OFF	ON	OFF	OFF	ON
HT48X50A-1-40/48 HT48E50-48	ON	OFF	OFF	OFF	OFF	OFF	OFF
HT48X70A-1-48/64 HT48E70-48/64	OFF	OFF	OFF	OFF	OFF	OFF	OFF
HT48XU80-48/64	OFF	ON	OFF	OFF	OFF	ON	OFF
HT48XA0-1-24	OFF	OFF	OFF	OFF	OFF	OFF	OFF
HT48XA0-2-24	OFF	OFF	OFF	OFF	OFF	OFF	OFF
HT48XA1/A3/A5-28	ON	OFF	OFF	ON	OFF	OFF	ON

Note: X indicates, ″C″ for mask devices or ″R″ for OTP devices.

The U1 to U8 and J1 connectors are used for different MCUs whose assignments are shown in the following table.

Socket	Supported IC
U1	HT48X07A-1, HT48X09A-1, HT48X10A-1, HT48E10 (24-pin)
U2	HT48X30A-1, HT48E30 (24-pin)
U3	HT48X30A-1, HT48X50A-1, HT48E30, HT48E50, HT48XA1, HT48XA3, HT48XA5 (28-pin)
U4	HT48X05A-1, HT48X06A-1, HT48X08A-1 (16-pin)
U5	HT48X05A-1, HT48X06A-1, HT48X08A-1, HT48E06 (18-pin)
U6	HT48X50A-1 (40-pin)
U8	HT48E06, HT48RA0-2 (20-pin) HT48RA0-1 (24-pin)
J1	HT48X50A-1, HT48E50, HT48X70A-1, HT48E70, HT48XU80 (48-pin) HT48X70A-1, HT48E70, HT48XU80 (64-pin)

Note: when using U8 to emulate the 20-pin packages, care must be taken with the respective connections.

CPCB48R52A006A Interface Card

This interface card is used in conjunction with the **CICE48R52A006A.**
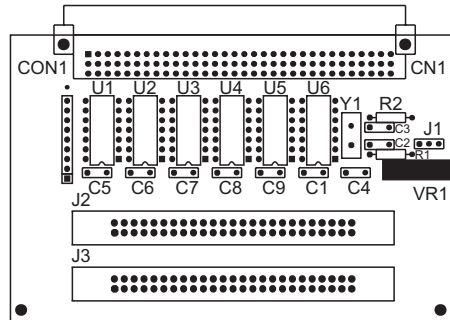
The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on J1 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR1. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection.
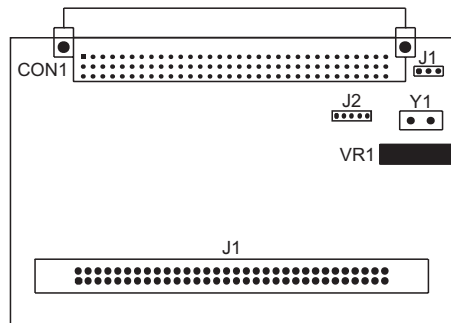
CPCB48R530004A Interface Card

This interface card is used in conjunction with the **CICE48R530004A.**

The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on J1 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR1.
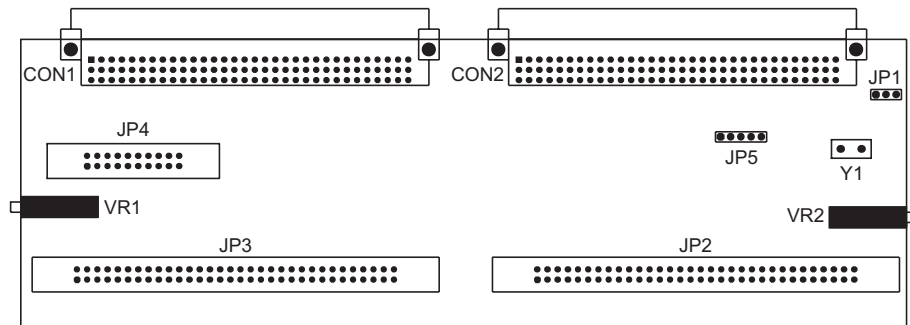
CPCB49C000001A Interface Card

This interface card is used in conjunction with the **CICE49C00CCAA**

The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on JP1 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR2. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection. VR1 is used to adjust the LCD voltage, VLCD.

Connector JP2 is used for the HT49R30A-1/HT49C30-1 devices with 48-pin packages while connector JP3 is used for the HT49R50A-1/HT49C50-1 devices with 48-pin packages. For the HT49R50A-1/HT49C50-1 devices with 100-pin packages and for the HT49R70A-1/HT49C70-1, it is necessary to use both the JP3 and JP4 connectors.

CPCB49VIO0004A Interface Card

This interface card is used in conjunction with the **CICE49CV00004A**, includes the following functions:

- External clock source
- A/D converter HT46V00 in location U7
- MCU pin assignments, I/O, VFD common and segment output connections
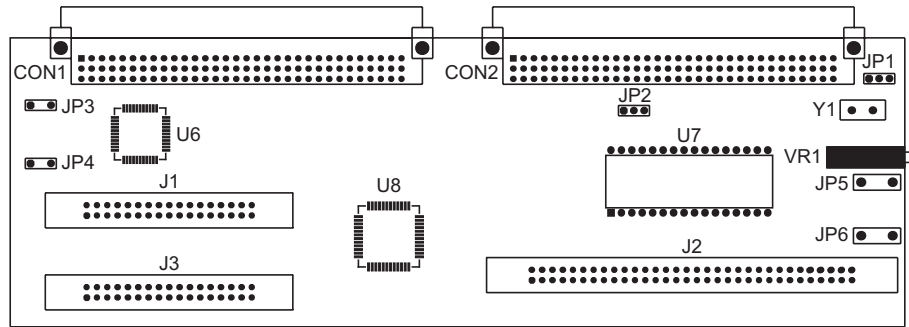- VFD driver HT16511/12

The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on JP1 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR1. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection.

Jumper JP2 is used to select the MCU's A/D converter AVDD power supply source. Positions 1 and 2 on JP2 should be shorted if the HT-ICE 5V supply voltage is to be used as the source. For other externally supplied AVDD voltages, positions 2 and 3 should be shorted, the required voltage can then be provided from JP5 and JP6.

The appropriate VFD voltage for the user application should be applied to JP3.
For the HT49XV3 device which has no A/D converter, the sections on the transfer card pertaining to the A/D have no function.
For the GRID/SEG connections on the VFD, the explanation on the transfer card should be followed to choose either JP1 or JP3. Normal outputs and inputs should be connected to JP2.

CPCB46R140004A Interface Card

This interface card must be used in conjunction with the correct ICE Hardware Emulator. This interface card includes the following functions:

- External clock source
- MCU pin assignments, I/O, LCD common and segment output connections

The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on J2 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR1. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection.
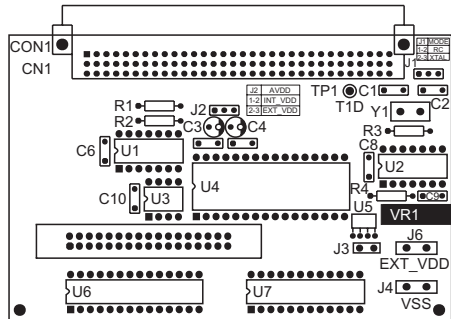
Jumper J2 is used to select the MCU's A/D converter AVDD power supply source. Positions 1 and 2 on J3 should be shorted if the HT-ICE 5V supply voltage is to be used as the source. For other externally supplied AVDD voltages, positions 2 and 3 should be shorted, the required voltage can then be provided from J4 and J6.

Note: The ICE supplies 5V, however if the target board requires a voltage other than 5V, then AVDD must use EXT_VDD which is supplied on J4 and J6 if the AD is to be correctly emulated.

CPCB46SER0001A Interface Card

This interface card, which is used in conjunction with the **CICE46C00CCEA**, includes the following functions:

- External clock source
- A/D converter HT46V00 or HTUY0001 in location U5
- MCU pin assignments, I/O, LCD common and segment output connections

The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on JP1 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR1. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection. VR2 is used to adjust the LCD voltage (VLCD).
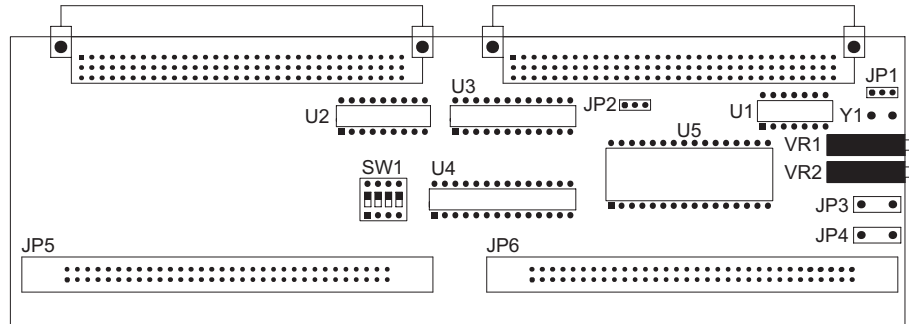
Jumper JP2 is used to select the MCU's A/D converter AVDD power supply source. Positions 1 and 2 on JP2 should be shorted if the HT-ICE 5V supply voltage is to be used as the source. For other externally supplied AVDD voltages, positions 2 and 3 should be shorted, the required voltage can then be provided from JP3 and JP4.

Note: Only the HT46R63 device has VADD, the other devices in the HT46 series MCU do not have this option. For these other devices VADD is the same as VDD.

DIP switch SW1 should be set according to which device is selected and in accordance with the following table:

SW1	1	2	3	4
HT46X24	OFF	OFF	OFF	OFF
HT46X62	ON	ON	ON	OFF
HT46X63	ON	ON	ON	OFF
HT46X64	ON	ON	ON	ON
HT46X65	ON	ON	ON	ON
HT46X22	Don't care			
HT46X23	Don't care			
HT46X47	Don't care			

The JP5 connector provides the LCD common and segment outputs while the JP6 connector provides the I/O port connections as well as other pins. The sockets at locations U2, U3 and U4 are used for other purposes by different MCUs. Their assignments are shown in the following table.

Socket	Supported IC
U2	HT46X47
U3	HT46X22, HT46X23 (24-pin)
U4	HT46X23 (28-pin)
JP5+JP6	HT46X62, HT46X63, HT46X64, HT46X65
JP6	HT46X24

An A/D converter HT46V00 or HTUY0001, is located at location U5 which is used to emulate the function of the internal MCU A/D converter.

CPCB46SER0001B Interface Card

This interface card must be used in conjunction with the correct ICE Hardware Emulator. If the UART function is to be emulated then only later ICE versions should be used. This interface card includes the following functions:

- External clock source
- A/D converter HT46V01 in location U5
- MCU pin assignments, I/O, LCD common and segment output connections
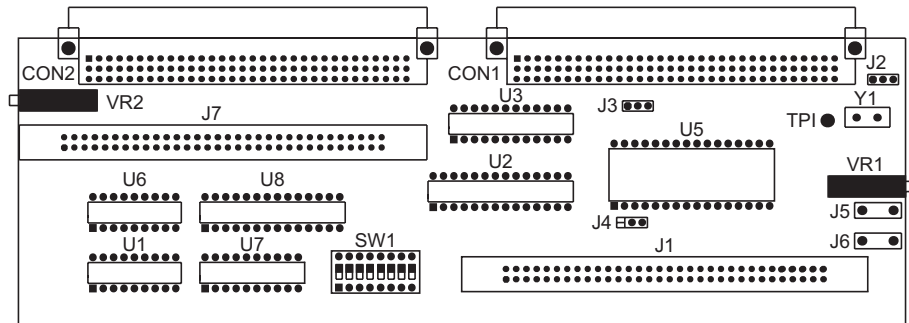
The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on J2 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR1. Refer to the Tools/Mask Option menu of the HT-IDE3000 User′s Guide for the clock source and system frequency selection. VR2 is used to adjust the LCD voltage (VLCD).

Jumper J3 is used to select the MCU′s A/D converter AVDD power supply source. Positions 1 and 2 on J3 should be shorted if the HT-ICE 5V supply voltage is to be used as the source. For other externally supplied AVDD voltages, positions 2 and 3 should be shorted, the required voltage can then be provided from J5 and J6.

Note: Only the HT46R63 device has AVDD, the other devices in the HT46 series MCU do not have this option. For these other devices AVDD is the same as VDD. The ICE supplies 5V, however if the target board requires a voltage other than 5V, then AVDD must use EXT_VDD which is supplied on J5 and J6 if the AD is to be correctly emulated.

The A/D converter reference voltage, VRFF, can be chosen with J4. If AVDD is chosen, then J4 should connect positions 1 and 2, if an external voltage source is to be used, then J4 should connect positions 2 and 3.

Note: Only the HT46X51, HT46X52 20-pin package devices and the HT46X53, HT46X54 28-pin packages have a VRFF pin. Therefore only these devices can chose the VRFF reference voltage. For the other devices in the 46 series, as VRFF and AVDD are connected together, therefore J4 should connect positions 1 and 2.

DIP switch SW1 should be set according to which device is selected and in accordance with the following table: (6, 7, 8 don't care)

SW1	1	2	3	4	5
Others	OFF	OFF	OFF	OFF	OFF
HT46X62	ON	ON	ON	OFF	OFF
HT46X63	ON	ON	ON	OFF	OFF
HT46X64	ON	ON	ON	ON	OFF
HT46X65	ON	ON	ON	ON	OFF
HT46XU66	ON	ON	ON	ON	OFF

The J7 connector provides the LCD common and segment outputs while the J1 connector provides the I/O port connections as well as other pins. The sockets at locations U2, U3, U4, U6, U7 and U8 are used for other purposes by different MCUs. Their assignments are shown in the following table.

Socket	Supported IC
U2	HT46X23, HT46X24, HT46X232 (28-pin)
U3	HT46X22, HT46X23 (24-pin)
U4	HT46X46/47, HT46X46E, HT46X47E (18-pin)
U6	HT46X51/52 (18-pin)
U7	HT46X51/52 (20-pin)
U8	HT46X53/54 (28-pin)
J1	HT46X24, HT46X232
J1+J7	HT46X62, HT46X63, HT46X64, HT46X65, HT46XU66

Note: when "X" use for "C" are mask devices

"R" are OTP devices

The part numbers with an "E" suffix contain an internal EEPROM. For proper emulation of the internal EEPROM, the four pins of an external HT2201 EEPROM must be connected to connector U9 on the Interface Card. Switch SW2, which is only used with devices which contain an EEPROM, must then be properly setup for the device and package used. As the location of the SDA and SCL pins is device and package dependent, SW2 is used to ensure proper connection to their correct I/O pins for correct emulation.

For example: In package SAD-PA7, SCL-PB3. Then SW2-1 dials —, SW2-2 dials +, other SW2 dials the code to be motionless in among.

In package SAD-PA3, SCL-PB4. Then SW2-2 dials—, SW2-3 dials +, other SW2 dials the code to be motionless in among.

Device	SW2							
	1	2	3	4	5	6	7	8
	PA7	PB3	PB4	PC0	PC1	PD0	x	x
HT46R46E (18DIP)	O	+	O	O	O	—	O	O
HT46R47E (18DIP)	O	+	O	O	O	—	O	O
HT46R48E (24DIP)	O	O	O	—	+	O	O	O
Others	O	O	O	O	O	O	O	O

The table entries, O, + and —, refer to the three possible positions on each of the switches of SW2, which is marked clearly on the switch.

TPCB47C00-A-1 Interface Card

This interface card is used in conjunction with the **TICE47C-CCDA**
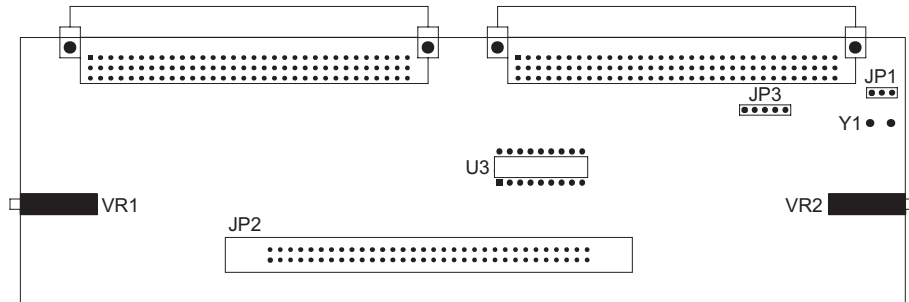
The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on JP1 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is used, positions 1 and 2 should be shorted and the system frequency adjusted using VR2. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection. VR1 is used to adjust the LCD voltage, VLCD.

An RC oscillator type A/D converter is located at U3. Use the converter RFADOSC-1 for the HT47R20A-1/HT47C20-1 devices and the converter RFADOSC-2 for the HT47C20L device. The JP2 connector provides the I/O port connections and the LCD common/segment outputs.

TPCB47C100000A Interface Card

This interface card is used in conjunction with the **TICE47C100000A**
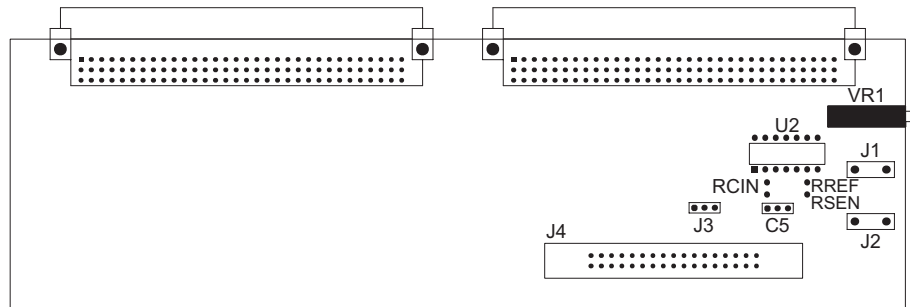
VR1 is used to adjust the LCD voltage (VLCD).

Two operating voltage sources can be selected, an internal 1.5V or external 1.5V. If the internal 1.5V is used, positions 1 and 2 on jumper J3 should be shorted. Otherwise, if the external 1.5V is used, positions 2 and 3 should be shorted, the power supply can then be connected to J1 and J2. The proper resistors must be selected for RCIN-RREF and RCIN-RSEN, and a capacitor for C5. Refer to the RC type A/D converter of the HT47C10L data sheet for more detailed information. An A/D converter, the HTK025 is located at U2.

CPCB950000005A Interface Card

This interface card is used in conjunction with the **CICE950000005A**
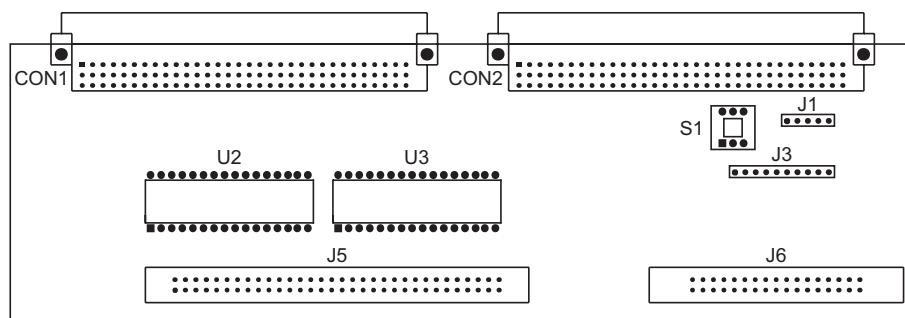
Switch S1 is used to simulate a low voltage condition, a situation which can be detected internally by the HT95C400, HT95C300, HT95C200, HT95L400/40P, HT95L300/30P, HT95L200/20P and HT95L100/10P devices. When switch S1 is pressed, the low battery flag (LBFG; bit 4 of LCDC register) will be set, resulting in a simulated low battery condition.

For the J5 and J6 connectors, as the devices in the HT95 series have different package types, the common pads are also different. The table below provides the necessary details.

Function	L000	L100	L200	L300	L400	C200	C300	C400
LCD COM	8	8	16	16	16	16	16	16
LCD SEGMENT	16	20	24	48	48	24	48	48
PD7~PD0 Shared COM			7~0			7~0		
PD7~PD4 Shared SEG				43~40	43~40		43~40	43~40
PD3~PD0 Shared SEG				39~36	39~36		39~36	39~36
PE3~PE0 Shared SEG	47~44	47~44		47~44	47~44		47~44	47~44
INT/TMR1 Shared Pad		Y	Y	Y	Y	Y	Y	Y
I/O	PAx8 PBx6 PEx4	PAx8 PBx8 PEx4	PAx8 PBx8 PDx8 PEx4	PAx8 PBx8 PDx8 PEx4	PAx8 PBx8 PDx8 PEx4 PFx8 PGx4	PAx8 PBx8 PDx8 PEx4	PAx8 PBx8 PDx8 PEx4	PAx8 PBx8 PDx8 PEx4 PFx8 PGx4

Description

On the interface card, for the LCD, no actual signal pins are provided as the LCD is fully simulated by the HT-IDE300 software. For the LCD common I/O pins, this is controlled by the related bits in the LCDIO register, for more details consult the HT95 datasheet.
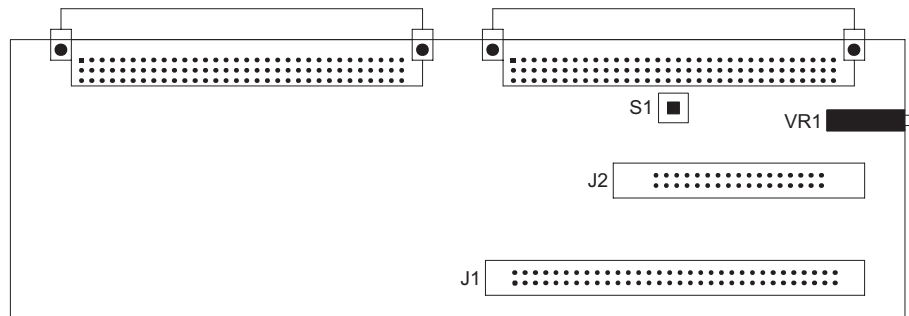
TPCBG2190-A Interface Card

This interface card is used in conjunction with the **TICEG2190-CCAA**
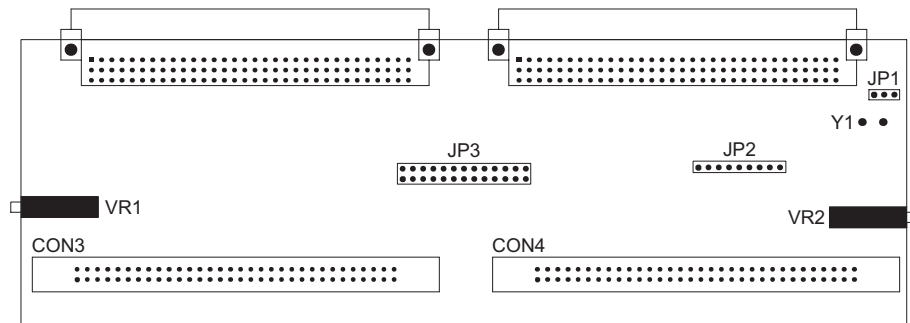
The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on JP1 and a suitable crystal inserted in location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR2. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection. VR1 is used to adjust the LCD voltage (VLCD).

TPCB23B60-A-1 Interface Card

This interface card is used in conjunction with the **TICE23B60-CCXA**

VR1 is used to adjust the LCD voltage, VLCD.

The J1 connector provides the LCD common and segment outputs while the J2 connector provides the I/O port connections as well as other pins.

If S1is moved down into the locked position, it simulates an LBIN pin voltage that is less than the low voltage detection level. In other words the LCDC register bit4, LVFG=1. On the other hand if S1 is in the unlocked state, that is LVFG=0, this will emulate a LBIN pin voltage that is greater than the low voltage detection level.

CPCB860000004A Interface Card

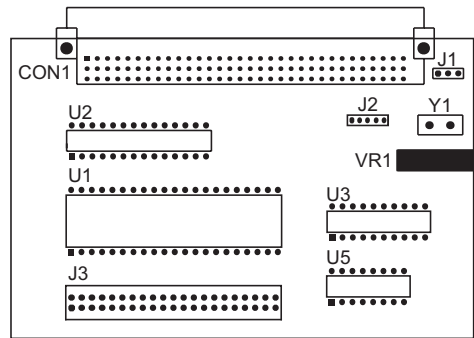This interface card is used in conjunction with the **CICE860000004A**
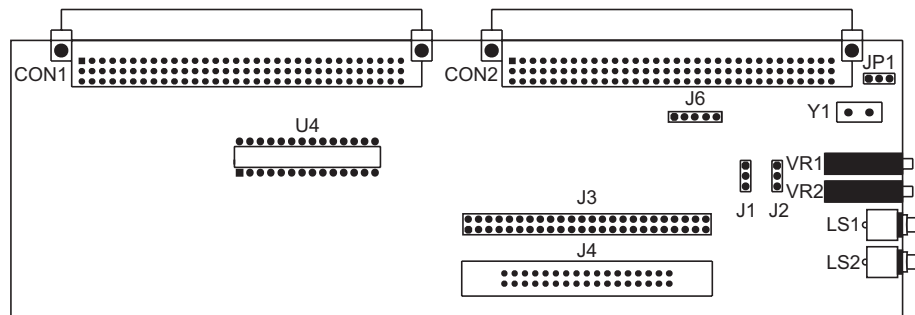
The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on JP1 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR1. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection.

A speaker can be attached to LS1 or LS2 to obtain an audio voice output. If a higher quality amplified voice output is required, connect the speaker to LS1. Here the volume can be adjusted using VR2. However, a jumper must first be placed on J3 to select the port pin, which is to be used to enable the interface card's HT82V733 audio amplifier. The user program can now enable the HT82V733 amplifier by driving this same pin low. Otherwise, connect the speaker to LS2 only.

CPCB82K680004A Interface Card

This interface card is used in conjunction with the **CICE82K680004A**. The card should be plugged into the CN1 connector of the HT-ICE.


The external clock source has two modes, RC and Crystal. If a crystal clock is to be used, positions 2 and 3 should be shorted on J1 and a suitable crystal inserted into location Y1. Otherwise, if an RC clock is to be used, positions 1 and 2 should be shorted and the system frequency adjusted using VR1. Refer to the Tools/Mask Option menu of the HT-IDE3000 User's Guide for the clock source and system frequency selection.

The U2 connector is used for the 28-pin packages while the U3 connector is used for the 20-pin packages. The U4 connector supports all pin assignments for the 48-pin packages, however as it is not fully compatible with the actual 48-pin packages, the necessary adjustments must be made by the user. The U5 connector is used for the 16-pin packages
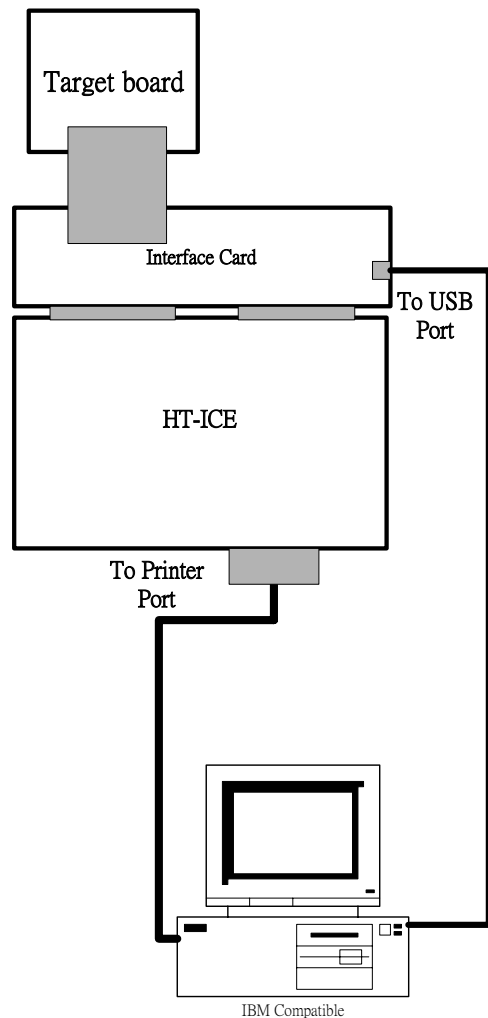
CPCB82K960004B Interface Card

This interface card is used in conjunction with the **CICE82K960004A**
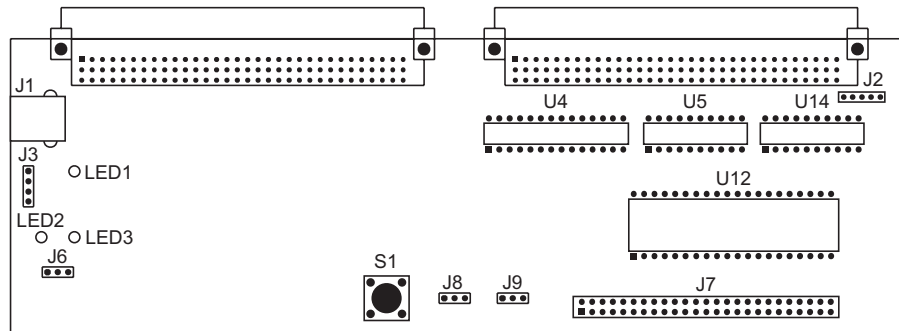
- HT-IDE3000 Configuration Option description:

  USB joystick: When the ADC function is to be used, the ″Enable joystick mode″ must be selected. After this has been completed, pins PB0~PB6 will be setup as A/D inputs, with the names, AN0~AN6, to the 8 bit ADC. Otherwise, select the ″Disable joystick mode″ which will allow the PB0~PB7 pins to be setup as normal I/O pins.

  Sys Freq: Do not select the ″Internal″ option. This is because the system frequency of the ICE is determined by the Interface Card settings.

- LED description:

  LED1: Yellow LED, when illuminated this indicates that the system is in its Active condition, when it is extinguished this indicates that the system in the Suspend condition.

  LED2: Red LED, when illuminated this indicates that the system frequency is 12MHz.

  LED3: Green LED, when illuminated this indicates that the system frequency is 6MHz.

- Jumper and Push Button description:

  J1 and J3: USB connector.

  J6: The frequency selection jumper. When in position 1-2, the 12MHz frequency is selected, when in position 2-3 the 6MHz frequency is selected. If the 6MHz frequency is selected, then the SYSCLK flag in the SCC register must be set to ″1″, if the 12MHz frequency is selected then the SYSCLK flag must be set to ″0″.

  J8 and J9: When using the ADC, to increase its flexibility an Internal Reference Voltage or an External Reference Voltage can be used. When using the Internal Reference Voltage, the ADREF flag in the USR register should be set to ″1″. Also J8 and J9 should be placed in position 2-3. If an External Reference Voltage is used, then the ADREF flag should be set to ″0″ and J8 and J9 should be placed in position 1-2. The VRH input is via PB7 while the VRL input is via PB6.

 S1: RESET input which corresponds to the MCU reset.

- Target board interface description:

  For interfacing to the user target board a choice of five connectors, U4, U5, U12, U14 and J7 are provided.

- Diagnose description:

  – Within the HT-IDE3000, select TOOL\Diagnose, then press the reset switch on the HT-ICE in accordance with the instructions.

  – Select Test All to enter the HT-ICE self-diagnostics. When the system enters the IO EV1 section, the message ″Press ′RESETB′ to test USB Connection″ will appear. Ensure first that the USB cable is connected correctly before pressing the S1 switch on the interface card. The USB connection will then be tested and if successful the message ″USB device connect successful″ will appear. If unsuccessful the message ″Connect USB Fail, Try Again?″ will appear.

  – When all of the tests are complete and the message ″Total Error 0″ appears, this indicates that there are no errors in the ICE. The Target Board can now be attached and used.

HT-ICE (CICE82K960004A) and I/O Interface Card Outline

CPCB82M990004A Interface Card

This interface card is used in conjunction with the **CICE82M990004A**
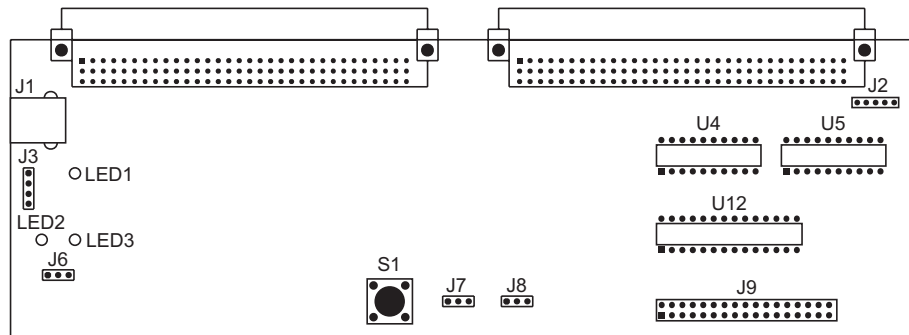
- HT-IDE3000 Configuration Option description:

  Sys Freq: Do not select the ″Internal″ option. This is because the ICE system frequency is determined by the Interface Card settings.

- LED description:

  LED1:   Yellow LED, when illuminated this indicates that the system is in its Active condition, when extinguished this indicates that the system in the Suspend condition.

  LED2:   Red LED, when illuminated this indicates that the system frequency is 12MHz.

  LED3:   Green LED, when illuminated this indicates that the system frequency is 6MHz.

- Jumper and Push Button description:

  J1 and J3: USB connectors.

  J6:   The frequency selection jumper. When in position 1-2, the 12MHz frequency is selected, when in position 2-3 the 6MHz frequency is selected. If the 6MHz frequency is selected, then the SYSCLK flag in the SCC register must be set to ″1″, if the 12MHz frequency is selected then the SYSCLK flag must be set to ″0″.

  J7 and J8: When using the ADC, to increase its flexibility an Internal Reference Voltage or an External Reference Voltage can be used. When using the Internal Reference Voltage, the ADREF flag in the USR register should be set to ″1″. Also J7 and J8 should be placed in position 2~3. If an External Reference Voltage is used, then the ADREF flag should be set to ″0″ and J7 and J8 should be placed in position 1~2. The VRH input is via PB7 while the VRL input is via PB6.

  S1:   RESET input which corresponds to the MCU reset

- Target board interface description:

  For interfacing to the user target board a choice of four connectors, U4, U5, U12, and J9 are provided.

- Diagnose description:

  − Within the HT-IDE3000, select TOOL\Diagnose, then press the reset switch on the HT-ICE in accordance with the instructions.

  − Select Test All to enter the HT-ICE self-diagnostics. When the system enters the IO EV1 section, the message ″Press ′RESETB′ to test USB Connection″ will appear. Ensure first that the USB cable is connected correctly before pressing the S1 switch on the interface card. The USB connection will then be tested and if successful the message ″USB device connect successful″ will appear. If unsuccessful the message ″Connect USB Fail, Try Again?″ will appear.

  − When all of the tests are complete and the message ″Total Error 0″ appears, this indicates that there are no errors in the ICE. The Target Board can now be attached and used.

HT-ICE (CICE82M990004A) and I/O Interface Card Outline

Target board

Interface Card

To USB Port

HT-ICE

To Printer Port

IBM Compatible