

## REPETITIVE AVALANCHE AND dv/dt RATED HEXFET<sup>®</sup> TRANSISTORS THRU-HOLE (TO-204AA/AE)

## IRF450 JANTX2N6770 JANTXV2N6770 500V, N-CHANNEL

### Product Summary

Part Number	BVDSS	RDS(on)	ID
IRF450	500V	0.400Ω	12A

The HEXFET<sup>®</sup> technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry and unique processing of this latest "State of the Art" design achieves: very low on-state resistance combined with high transconductance; superior reverse energy and diode recovery dv/dt capability.

The HEXFET transistors also feature all of the well established advantages of MOSFETs such as voltage control, very fast switching, ease of paralleling and temperature stability of the electrical parameters.

They are well suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high energy pulse circuits.



### Features:

- Repetitive Avalanche Ratings
- Dynamic dv/dt Rating
- Hermetically Sealed
- Simple Drive Requirements
- Ease of Paralleling

### Absolute Maximum Ratings

	Parameter		Units
ID @ VGS = 0V, TC = 25°C	Continuous Drain Current	12	A
ID @ VGS = 0V, TC = 100°C	Continuous Drain Current	7.75	
IDM	Pulsed Drain Current ①	48	
PD @ TC = 25°C	Max. Power Dissipation	150	W
	Linear Derating Factor	1.2	W/°C
VGS	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy ②	750	mJ
IAR	Avalanche Current ③	12	A
EAR	Repetitive Avalanche Energy ④	15	mJ
dv/dt	Peak Diode Recovery dv/dt ⑤	3.5	V/ns
TJ	Operating Junction	-55 to 150	°C
TSTG	Storage Temperature Range		
	Lead Temperature	300 (0.063 in. (1.6mm) from case for 10s)	
	Weight	11.5 (typical)	g

For footnotes refer to the last page

**Electrical Characteristics @ T<sub>j</sub> = 25°C (Unless Otherwise Specified)**

	Parameter	Min	Typ	Max	Units	Test Conditions
B <sub>V</sub> DSS	Drain-to-Source Breakdown Voltage	500	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1.0mA
ΔB <sub>V</sub> DSS/ΔT <sub>J</sub>	Temperature Coefficient of Breakdown Voltage	—	0.78	—	V/°C	Reference to 25°C, I <sub>D</sub> = 1.0mA
R <sub>DS(on)</sub>	Static Drain-to-Source On-State Resistance	—	—	0.4	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 7.75A (
		—	—	0.5		V <sub>GS</sub> = 10V, I <sub>D</sub> = 12A (
V <sub>GS(th)</sub>	Gate Threshold Voltage	2.0	—	4.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
g <sub>fs</sub>	Forward Transconductance	5.5	—	—	S	V <sub>DS</sub> > 15V, I <sub>DS</sub> = 7.75A (
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	—	—	25	μA	V <sub>DS</sub> = 400V, V <sub>GS</sub> = 0V
		—	—	250		V <sub>DS</sub> = 400V V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C
I <sub>GSS</sub>	Gate-to-Source Leakage Forward	—	—	100	nA	V <sub>GS</sub> = 20V
I <sub>GSS</sub>	Gate-to-Source Leakage Reverse	—	—	-100		V <sub>GS</sub> = -20V
Q <sub>g</sub>	Total Gate Charge	55	—	120	nC	V <sub>GS</sub> = 10V, I <sub>D</sub> = 12A
Q <sub>gs</sub>	Gate-to-Source Charge	5.0	—	19		V <sub>DS</sub> = 250V
Q <sub>gd</sub>	Gate-to-Drain ('Miller') Charge	27	—	70		
t <sub>d(on)</sub>	Turn-On Delay Time	—	—	35	ns	V <sub>DD</sub> = 250V, I <sub>D</sub> = 12A, V <sub>GS</sub> = 10V, R <sub>G</sub> = 2.35Ω
t <sub>r</sub>	Rise Time	—	—	190		
t <sub>d(off)</sub>	Turn-Off Delay Time	—	—	170		
t <sub>f</sub>	Fall Time	—	—	130		
L <sub>S</sub> + L <sub>D</sub>	Total Inductance	—	6.1	—	nH	Measured from drain lead (6mm/ 0.25in. from package) to source lead (6mm/0.25in. from package)
C <sub>iss</sub>	Input Capacitance	—	2700	—	pF	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V f = 1.0MHz
C <sub>oss</sub>	Output Capacitance	—	600	—		
C <sub>rss</sub>	Reverse Transfer Capacitance	—	240	—		

**Source-Drain Diode Ratings and Characteristics**

	Parameter	Min	Typ	Max	Units	Test Conditions
I <sub>S</sub>	Continuous Source Current (Body Diode)	—	—	12	A	
I <sub>SM</sub>	Pulse Source Current (Body Diode)	—	—	48		
V <sub>SD</sub>	Diode Forward Voltage	—	—	1.7	V	T <sub>j</sub> = 25°C, I <sub>S</sub> = 12A, V <sub>GS</sub> = 0V (
t <sub>rr</sub>	Reverse Recovery Time	—	—	1600	ns	T <sub>j</sub> = 25°C, I <sub>F</sub> = 12A, di/dt ≤ 100A/μs
Q <sub>RR</sub>	Reverse Recovery Charge	—	—	14	μC	V <sub>DD</sub> ≤ 50V (
t <sub>on</sub>	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L <sub>S</sub> + L <sub>D</sub> .				

**Thermal Resistance**

	Parameter	Min	Typ	Max	Units	Test Conditions
R <sub>thJC</sub>	Junction to Case	—	—	0.83	°C/W	Typical socket mount
R <sub>thJA</sub>	Junction to Ambient	—	—	30		

Note: Corresponding Spice and Saber models are available on the International Rectifier Website.

For footnotes refer to the last page

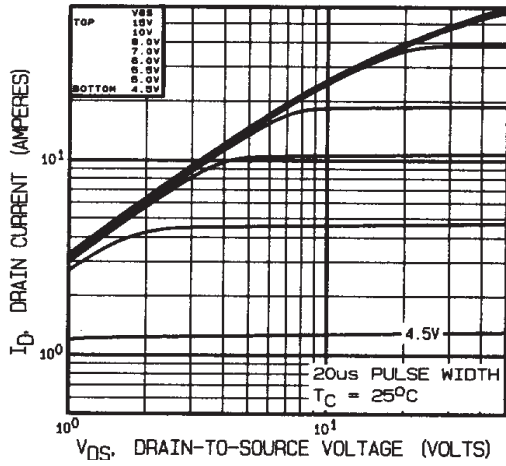


Fig 1. Typical Output Characteristics

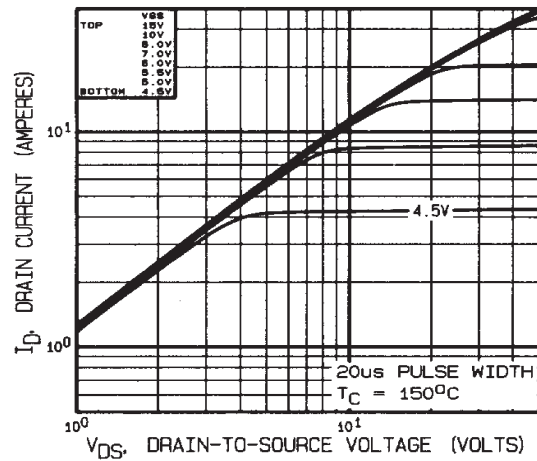


Fig 2. Typical Output Characteristics

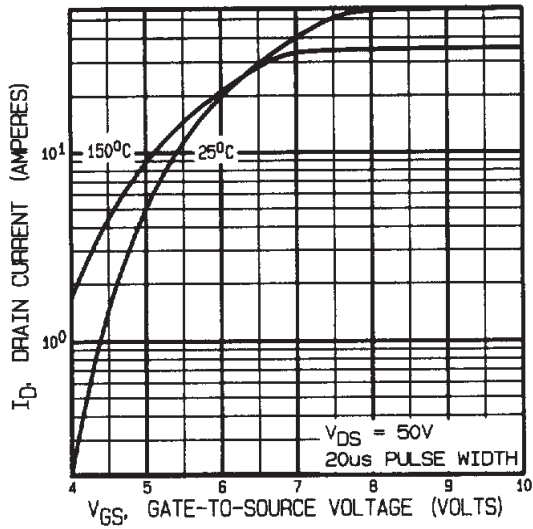


Fig 3. Typical Transfer Characteristics

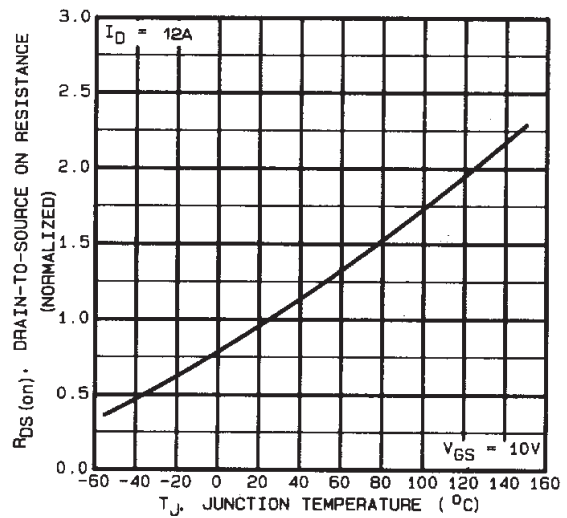


Fig 4. Normalized On-Resistance Vs. Temperature

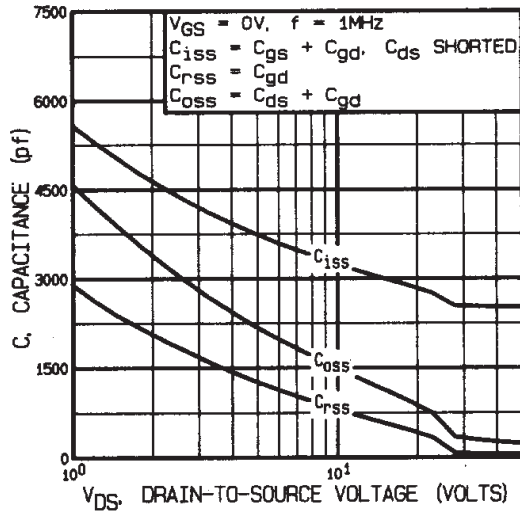


Fig5. Typical Capacitance Vs. Drain-to-Source Voltage

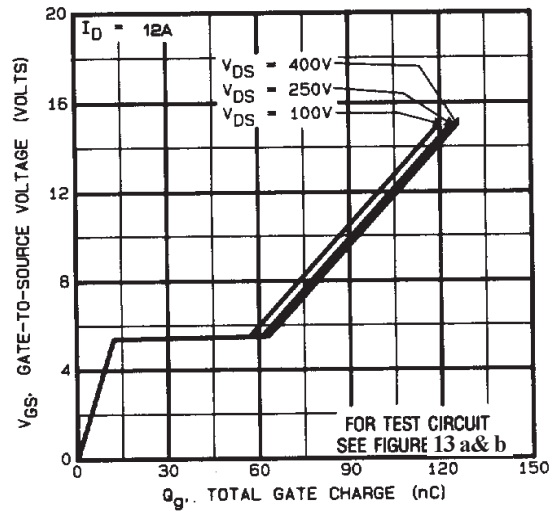


Fig6. Typical Gate Charge Vs. Gate-to-Source Voltage

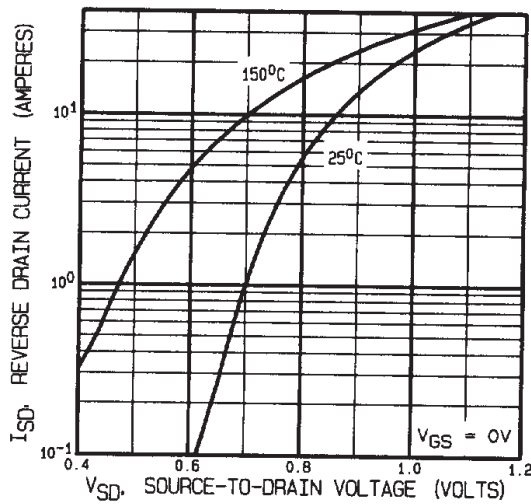


Fig7. Typical Source-Drain Diode Forward Voltage

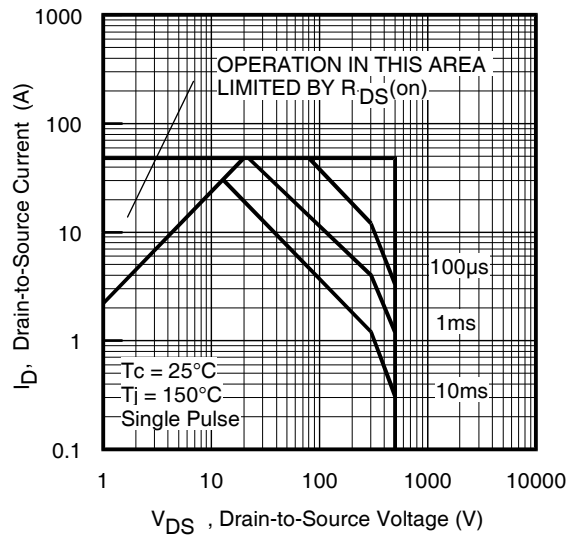


Fig8. Maximum Safe Operating Area

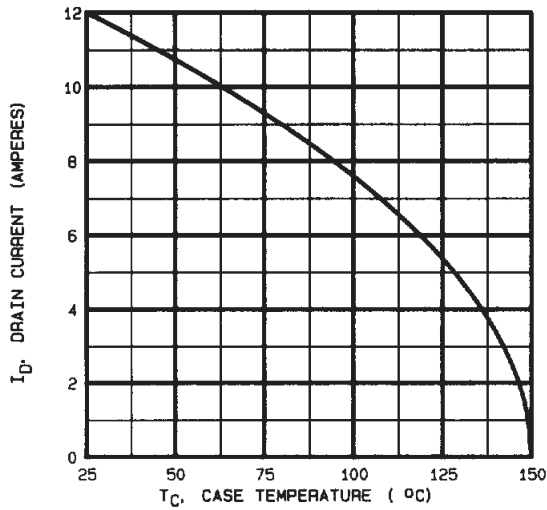


Fig9. Maximum Drain Current Vs. Case Temperature

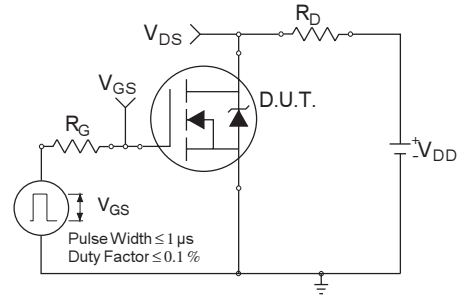


Fig 10a. Switching Time Test Circuit

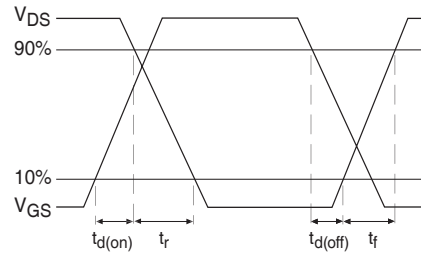


Fig 10b. Switching Time Waveforms

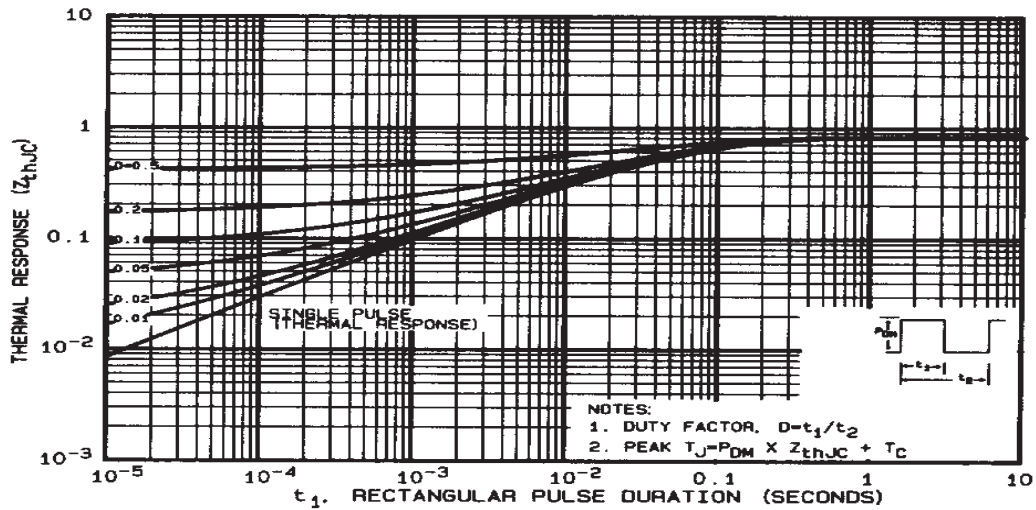


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

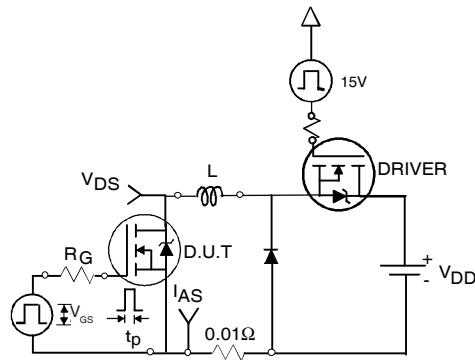


Fig 12a. Unclamped Inductive Test Circuit

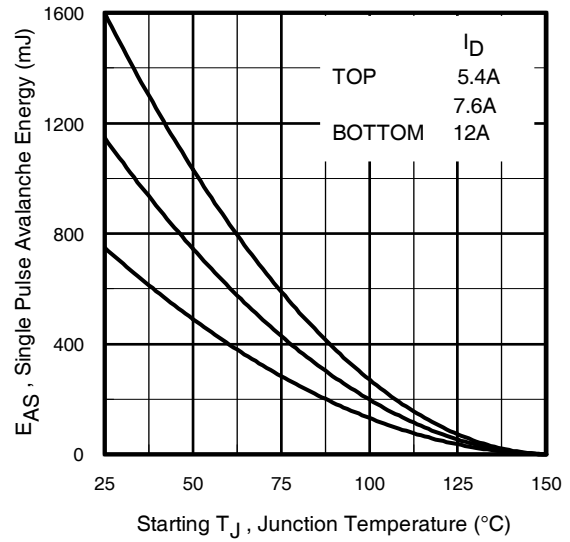


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

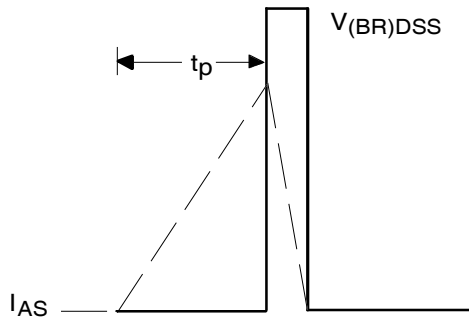


Fig 12b. Unclamped Inductive Waveforms

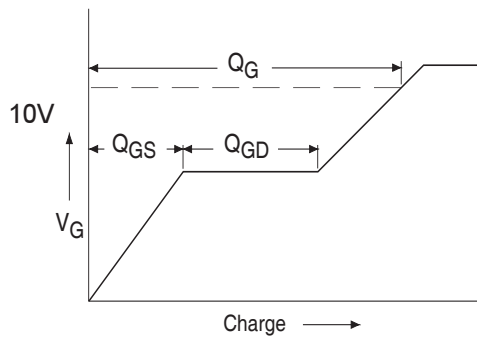


Fig 13a. Basic Gate Charge Waveform

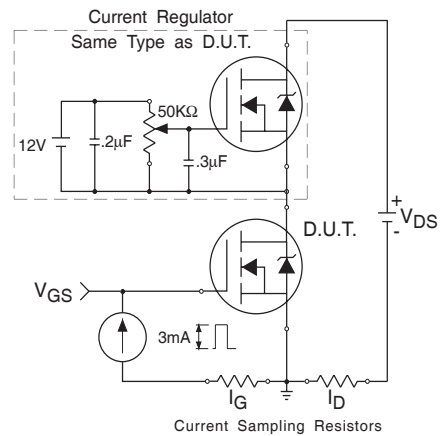


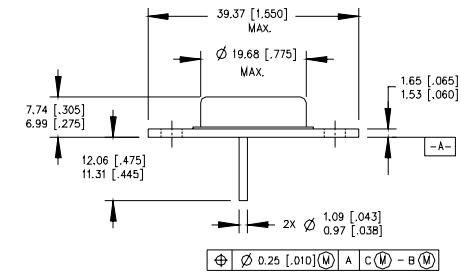
Fig 13b. Gate Charge Test Circuit

**Foot Notes:**

- ) Repetitive Rating; Pulse width limited by maximum junction temperature.
- (  $V_{DD} = 50V$ , starting  $T_J = 25^\circ C$ ,  
 Peak  $I_L = 12A, V_{GS} = 10V, L = 10.4mH$ .

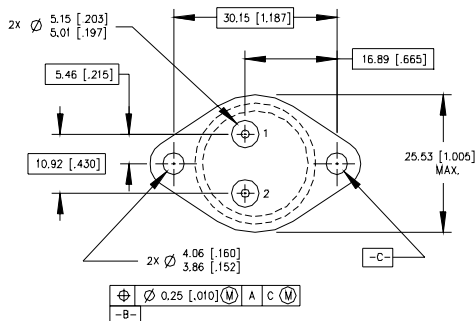
- )  $I_{SD} \leq 12, di/dt \leq 130A/\mu s,$   
 $V_{DD} \leq 500V, T_J \leq 150^\circ C$   
 Suggested  $R_G = 2.35 \Omega$
- ( Pulse width  $\leq 300 \mu s$ ; Duty Cycle  $\leq 2\%$

**Case Outline and Dimensions —TO-204AA (Modified TO-3)**



PIN ASSIGNMENTS

- HEXFET  
 1 - SOURCE  
 2 - GATE  
 3 - DRAIN (CASE)



NOTES:

1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1982.
2. CONTROLLING DIMENSION : INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. OUTLINE CONFORMS TO JEDEC OUTLINE TO-204-AA.